

SHRUTI THAKUR

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EDUCATION

Chitkara University

B.Tech. in Computer Science

2012 - 2016

CGPA: 8.01 / 10

SKILLSET

JavaScript, React, C++, Python, PyTorch, Java, PHP, Git and Android

PROFESSIONAL EXPERIENCE

Toshiba Software Pvt. Ltd.

Senior Software Engineer

2016 - 2020

Bangalore, India

- Led a team of **4** software engineers to develop printer management application for a new printer launch from scratch which is currently used by **half a million users daily**
- Developed various customer-facing workflows for web-based printer management application for multiple printer models using **ReactJS** improving installation and management time by 30% on average
- Implemented multiple performance and bug fixes in C++ printer software suite backend which directly improved customer turnover rate **by 9%**
- Building and maintaining critical features for other printer applications

EH1 Infotech

Software Developer and Java Coach

Jan - May 2016

Chandigarh, India

- **Developed** backend for various web applications using Java Spring framework
- Mentored new trainees in Java principles through lectures, assignments, and projects
- Built the schema for the database along with backend plugins using **MySQL**

9i Technologies

Software Engineering Intern

June - August 2014

Chandigarh, India

- Built a **shopping portal** replicating Amazon providing all necessary eCommerce features
- Developed a **CMS** for the eCommerce customer service representatives
- Developed using PHP, HTML, MySQL, and JavaScript (LAMP stack).

PERSONAL PROJECTS

Traffic Signs Recognition System

[Code](#)

- Achieved 99.6% classification accuracy on GTSRB dataset using a CNN-based network built in PyTorch.
- Increased effective dataset size by using heavy data augmentation including class balanced sampling, color jitter, rotation, shearing, affine and projection transformations.
- Implemented multiple models to achieve best performance using Spatial Transformer Network with IDSIA network along with added dropout and batch normalization.

Advanced Memory Management Unit

[Code](#)

- Designed a virtual memory manager mapping virtual address spaces onto physical frames and can handle multiple processes each with its own virtual address space.
- Implemented various paging algorithms for deciding next page to be replaced such as FIFO, NRU, Clock and Aging while making use of proper data structures such page table entry, and frame table.

AWARDS AND EXTRACURRICULAR

Zonal Debate Competition Organizer and Winner

Bangalore Debate Society

October 2016 - May 2019

Cultural and D&I Committee, Toshiba

Event and Relationship Manager

March 2017 - September 2019